



memory mapping

address	27512	2764	55257
0000	bootstrap + nibl basic	bootstrap + nibl basic	not used
8k			
1FFF	not used	used	ram memory pages
2000			
3FFF	not used	used	ram memory pages
4000			
5FFF	not used	used	ram memory pages
6000			
7FFF	perm. memt basic program	not used	ram memory pages
8000			
9FFF	not used	used	ram memory pages
A000			
BFFF	reserved for floating point basic	not used	ram memory pages
C000			
DFFF	reserved for floating point basic	not used	ram memory pages
E000			
FFFF	reserved for floating point basic	not used	ram memory pages

8255 IO

A000 = port A
A001 = port B
A002 = port C
A003 = control reg.